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Li Mei Vemilya (Depositor's name) 0/102 2006 January (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/003,390	10/30/2001	Tac-Yong Teong	4591-220	5348

TITLE OF INVENTION: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE WITH A FAIL BIT DETECTING SCHEME AND METHOD FOR COUNTING THE NUMBER OF FAIL BITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEB	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400		\$300	\$1700	01/24/2006
EXAMINER		ART UNIT		CLASS-SUBCLASS	7	
TU, CHRISTINE TRINH LE		2133		714-704000	-	
CFR 1.363). Change of corresponded responded	ation (or "Fee Address" Indic- or more recent) attached. Us D RESIDENCE DATA TO B as an assignee is identified be in 37 CFR 3.11. Completion	Correspondence ation form e of a Customer E PRINTED ON Telow, no assignee of this form is NO.	(1) the r or agent (2) the r registere 2 registed listed, no THE PATER data will at a substitut) RESIDEN	mear on the patent. If an accid	a member a mes of up to f no name is 2 MCCOL	R JOHNSON & LOM, P.C.
	te assignee category or catego	ries (will not be pr	inted on the	patent): 🔲 Individual 🛅 (Corporation or other private gr	oup entity 🔲 Government
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Date: January 24, 2006

Li Mei Vermilya

PATENT APPLICATION Docket No.: 4591-220 Client Ref. No.: ID10069-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yong JEONG and Sung-Soo LEE

Serial No.:

10/003,390

Examiner:

Tu, Christine Trinh Le

Filed:

October 30, 2001

Group Art Unit:

2133

Confirmation No.:

5348

For:

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE WITH A

FAIL BIT DETECTING SCHEME AND METHOD FOR

COUNTING THE NUMBER OF FAIL BITS

TRANSMITTAL LETTER

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Enclosed for filing in the above-referenced application are the following:

X Publication (\$300.00) and Issue (\$1400.00 large) fees

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Supplemental Declaration

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Respectfully submitted,

MARCER JOHNSON & McCOLLOM, P.C.

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